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FIELD PROGRAMMABLE PRINTED CIRCUIT BOARD

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SUBMISSION OF FORMAL DRAWINGS

Sir:

Enclosed for the above patent application are twenty (20) sheets of formal drawings. The enclosed formal drawings incorporate the changes requested in the preliminary amendment, dated 12 April 1996, to the drawings, and are believed to conform to the PTO Form 948 attached to Paper No. 4. If the enclosures are acceptable, please substitute them for the informal drawings now on file. If there are any questions regarding these drawings, please telephone Applicant's attorney at 408-453-9200, ext. 1371.

- 1 -

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on May 19, 2000.

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Attorney for Applicant(s)

Date of Signature

Respectfully submitted,

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630345 v1

SER. NO. 08/632,298



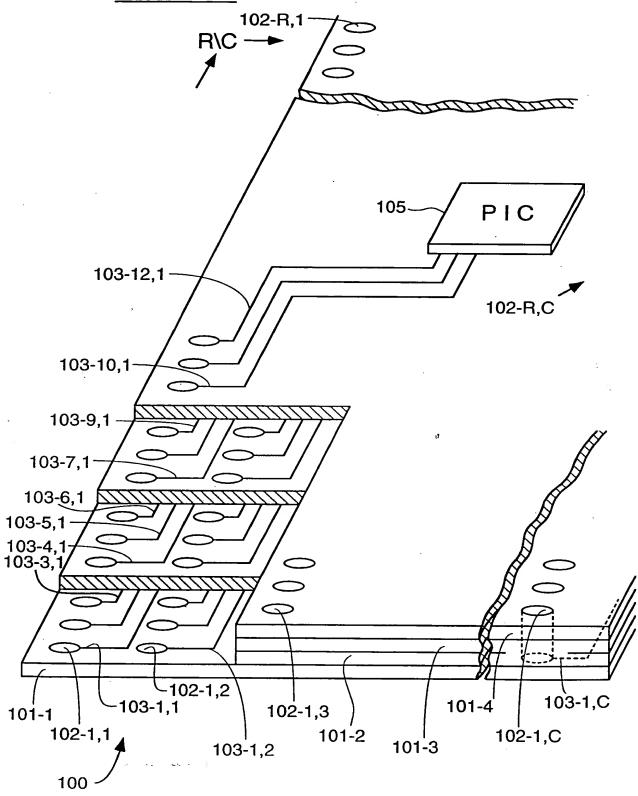
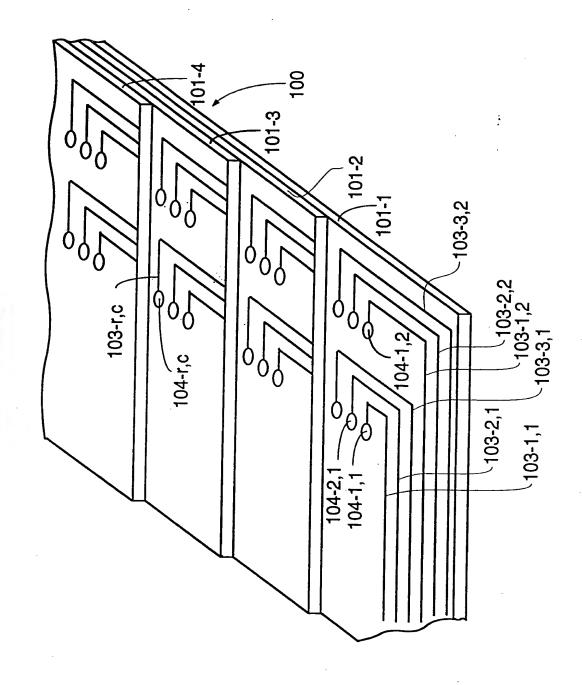


FIGURE 1b



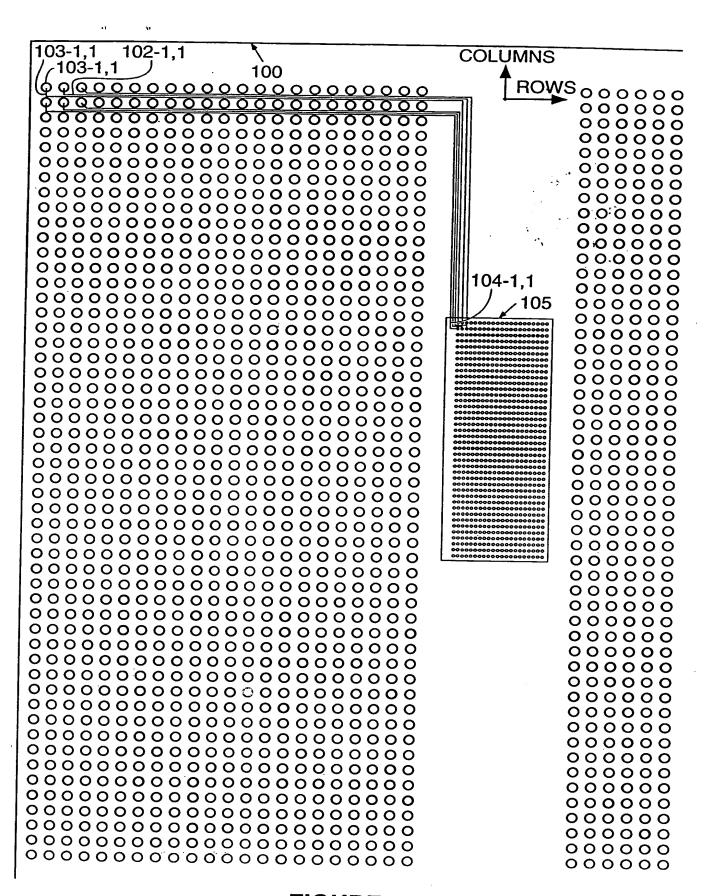


FIGURE 2a

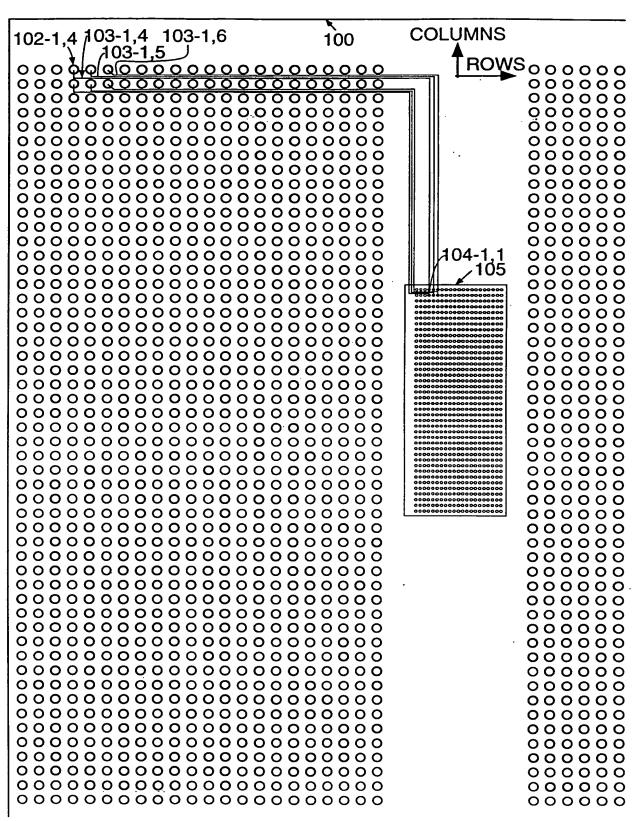


FIGURE 2b

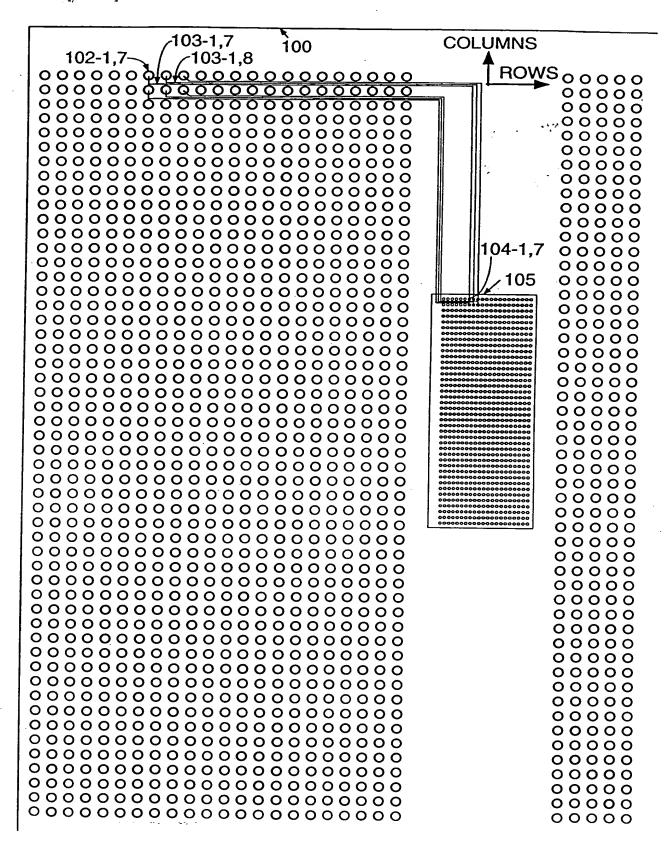


FIGURE 2c

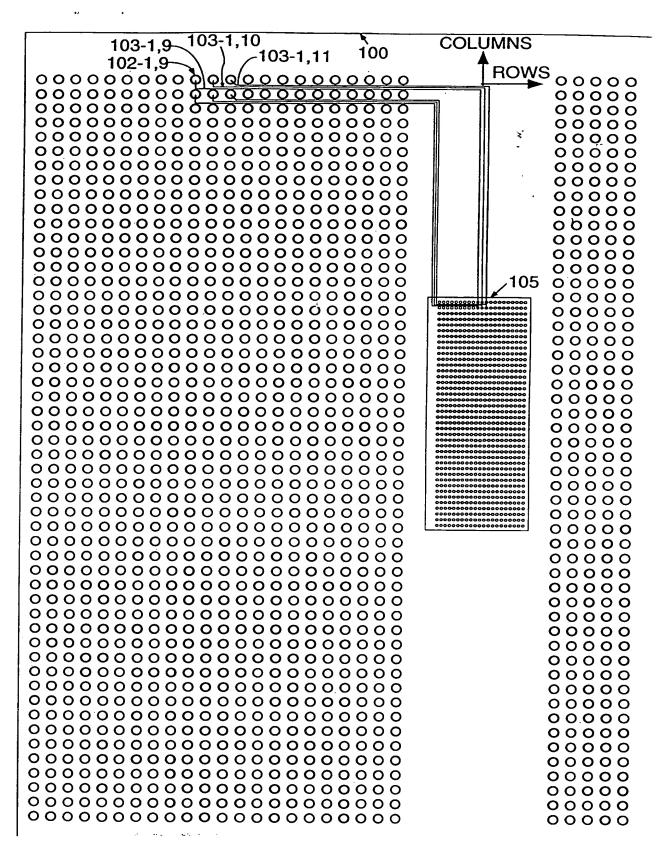


FIGURE 2d

GLOBAL INTERCONNECT ARCHITECTURE

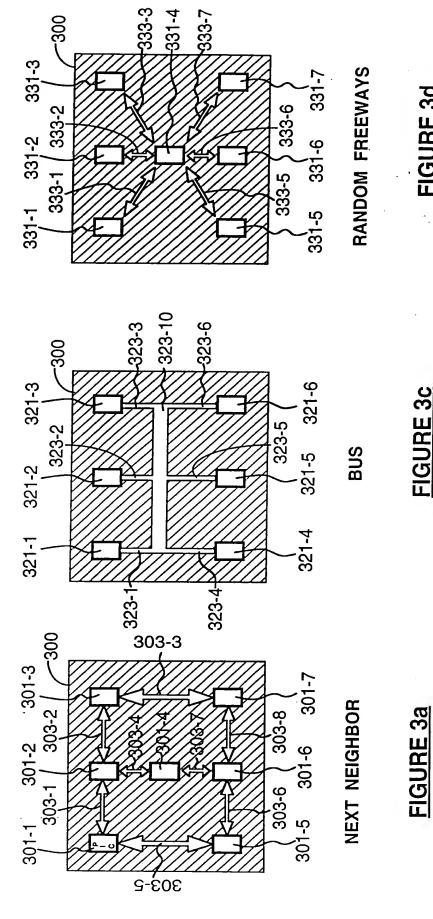
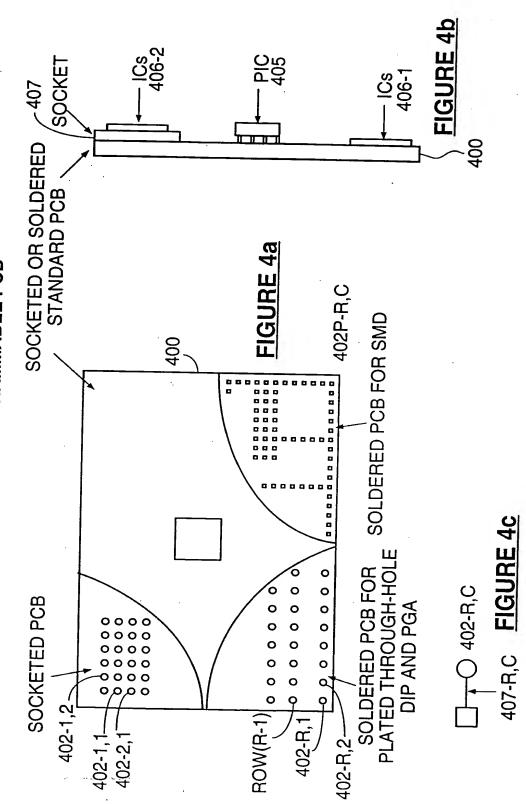


FIGURE 3d

-300-5 9 30 9 HIERARCHY OF GLOBAL INTERCONNECTS 311-3 311-2-**NEXT NEIGHBOUR** 313-3 -313-6 313-1 313-5~ 311-1 311-4 313-4~

FIGURE 3b

FIELD PROGRAMMABLE PCB



CONTROL PORT AND TEST PORT/ DIAGNOSTIC TOOL

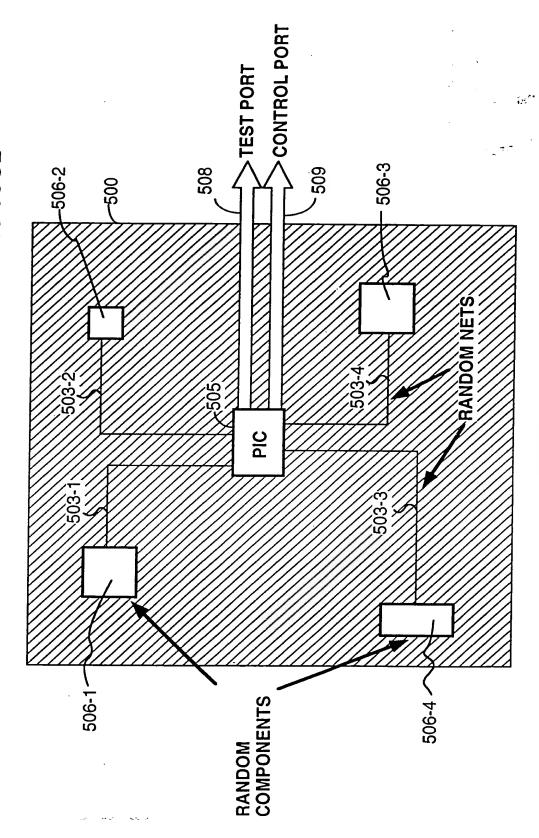
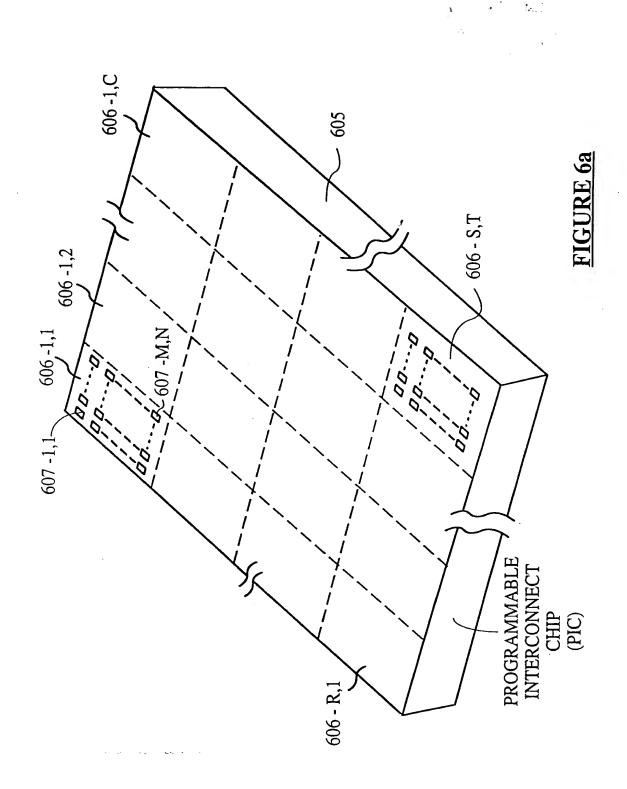


FIGURE 5



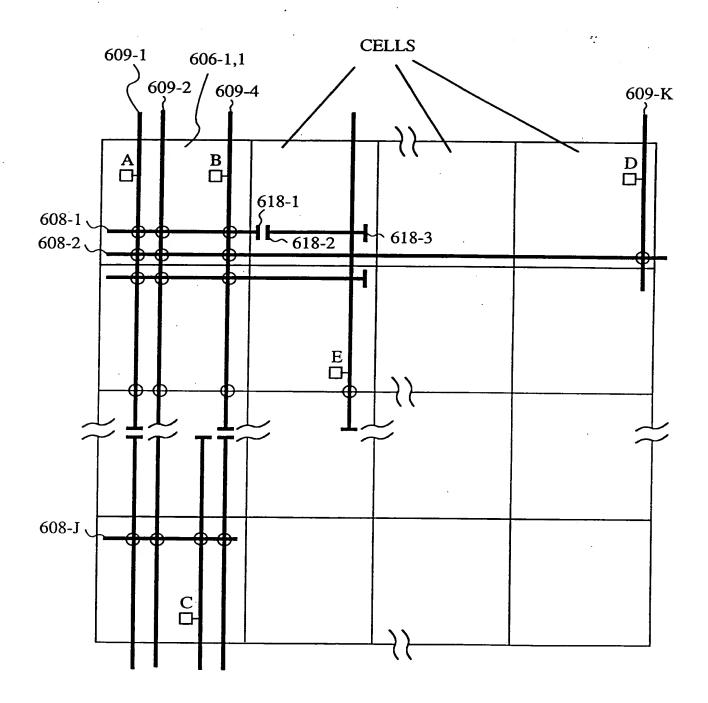
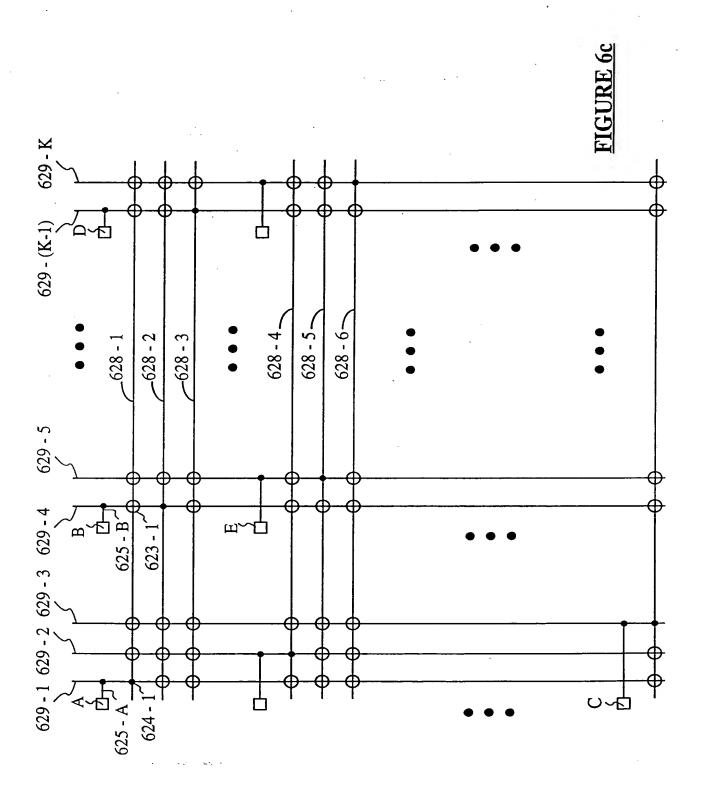
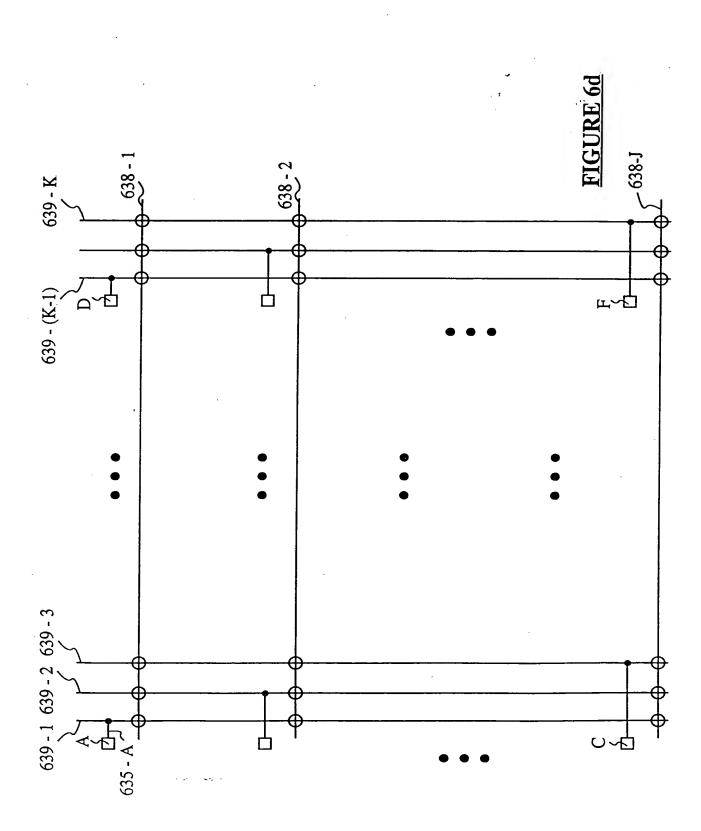
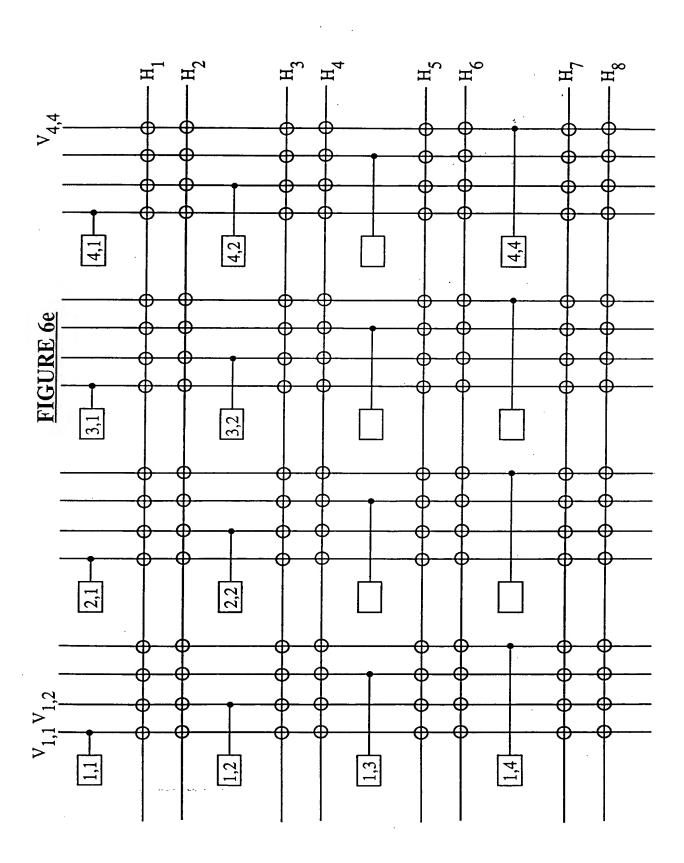


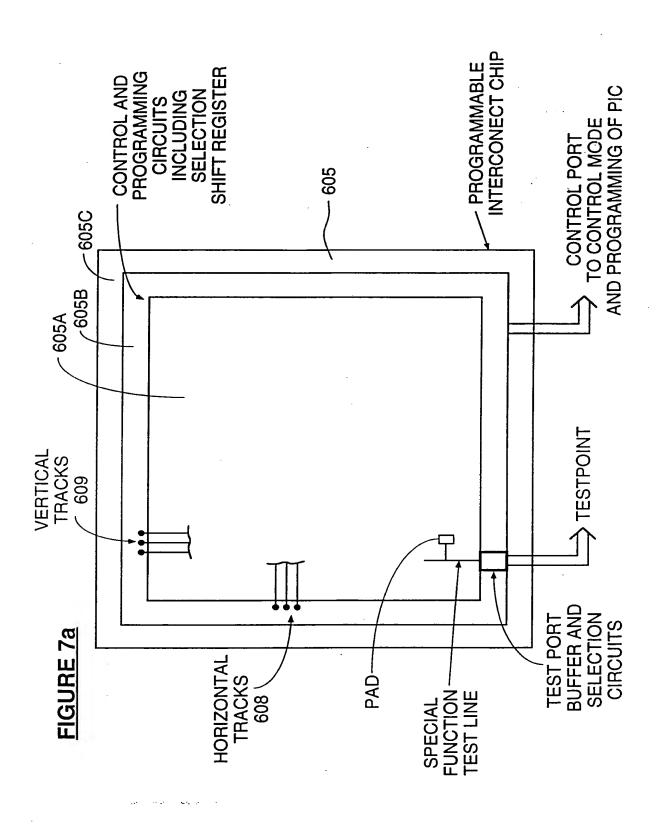
FIG. 6b



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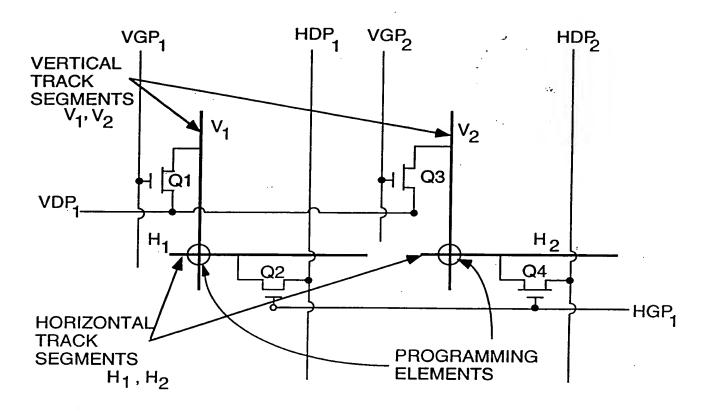


FIGURE 7b

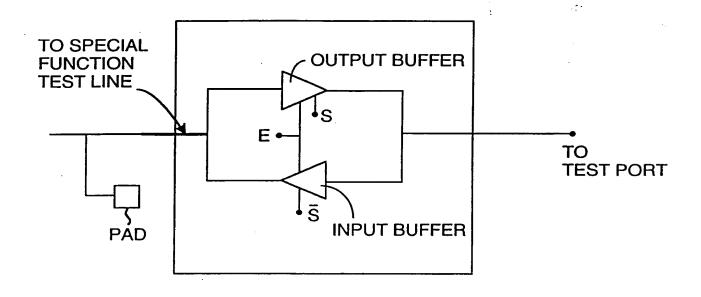
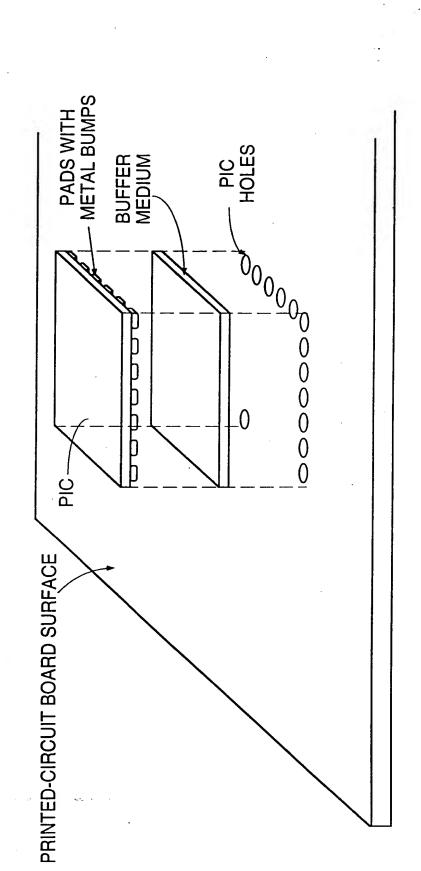
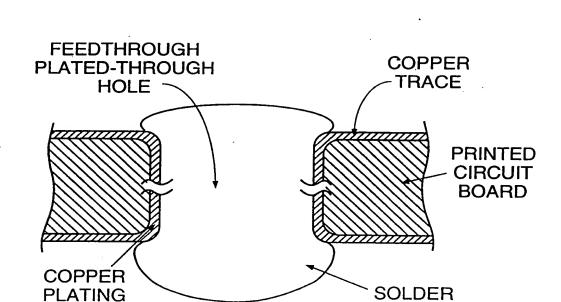


FIGURE 7c





BUMP

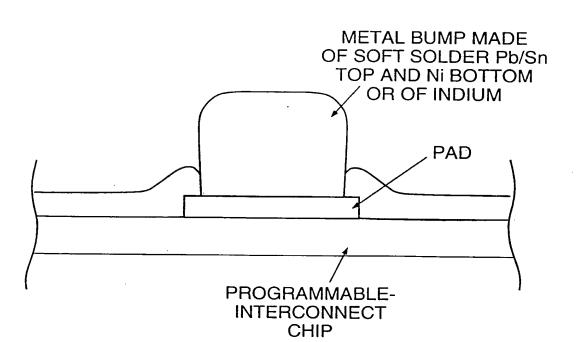


FIGURE 8b2

FIGURE 8b1